Sheet 1 of 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. TI-35424 (1962-05403)		SERIAL NO. NOT YET ASSIGNED	
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			U.S. PATF	ENT DOCUMENT	TS			_
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAM	E	CLASS	SUB- CLASS	FILING DATE (IF APPROPRIATE)
5.6	AA	6,098,089	08/01/00	O'Connor et	al.	709	104	April 23, 1997
SG	AB	6,567,905	05/20/03	Otis		711	170	January 23, 2001
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2.6	AF	2003/0101320	05/29/03	Chauvel et a	al.	711	154	May 29, 2002
			FOREIGN PA	TENT DOCUME	ENTS			
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5.6	AG	Embedded JAVA, V Online http://www.					abedded.h	tml
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete If Known					
Application Number	10/632,076				
Filing Date	07/31/2003				
First Named Inventor	Serge Lasserre, et al.				
Group Art Unit	2188				
Examiner Name	Padmanabhan, Mano				
Attomey Docket No.	71-35424				

				U.S. PATENT DOCUM	IENTS	
		U.S. Patent [Document	Name of Patentee or Applicant of	Date of Pub. of	
Exam. Initials	Cite No. ¹	Number	Kind Code ² (if known)	Cited Doc.	Cited Doc. (mm-dd-yy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
$\subset \mathcal{G}$	AA	5,893,148		Genduso, et al.	04/06/1999	
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		Office ³	Number ⁴	Kind Code ² (if known)				
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Application Number 10/632,076 Filing Date 07/31/2003 First Named Inventor Serge Lasserre, et al. **Group Art Unit** 2188 **Examiner Name** Padmanabhan, Mano Attorney Docket No. TI-35424

		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
G	CA	"Line Fetch/Prefetch in A Stack Cache Memory", Microprocessors and Microsystems, IPC Business Press Ltd., London, GB, Vol. 17, No. 9, 11/01/1993, pp. 547-555, XP000413173, ISSN: 0141-9331	
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